



Sandip Foundation's

Sandip Institute of Technology and Research Centre, Nashik

Department of Electronics and Telecommunication Engineering

International Conference on Deep Learning, IOT, Drone Technology, Smart Cities & Application (ICDIDSA-2023)

About Institute: The flagship institute of Sandip Foundation's Sandip Institute of Technology and Research Centre (SITRC) is established in 2008 and is NAAC accredited 'A' Grade with CGPA 3.11 and NBA accredited institute. The institute offer different UG, PG and Ph.D. programs and is approved by All India Council of Technical Education, New Delhi and recognized by Government of Maharashtra while being affiliated to Savitribai Phule Pune University, Pune. SITRC is recognized as one of the leaders in Engineering Education Research and Application of knowledge for the betterment of society and the country. The College has imprinted a position in the Engineering Education by creating graduates to the industry and the society. The modern infrastructure has been continuously motivated to permanent marked in the education field by creating technocrats , sound in academics and value system. The institute is located in a tranquil environment appropriate for constant learning. The highly efficient faculty at SITRC boosts the morale of students and makes them go to step beyond the ordinary and also harness campus placements.

About Department: Department of Electronics and Telecommunication Engineering has been established in the year 2008, with an intake of 60 students. Department also offers PG in VLSI & Embedded System and Ph.D. Program. The Department has well experienced and well qualified senior faculty member specified in VLSI, MEMS, Digital Image Processing, Embedded System, Wireless Network , Optical Communication & Digital Communication. The Electronics &Telecommunication Engineering department has well established laboratories with the state of art facilities. Electronics and circuit laboratory has been modernized under the AICTE scheme. The students are imparted with profound knowledge and practical skills and are motivated to undergo internship/training in India and abroad. The alumni of department scattered in various India and abroad pursuing higher studies in reputed



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university/institution and occupying higher position in MNC. The Head of Department of Electronics & Telecommunication Engineering is dealing by Dr. Gayatri M. Phade.

The Electronics & Telecommunication Engineering Department has 9 spacious laboratories. The department established Research lab for PG Scholar and Research Scholar. The lab has advanced software and EDA tools such as MATLAB, Cadence, TCAD, and PROTEUS such as Spectrum Analyzer, DSP Processor, and Antenna System Analyzer for the development of projects. Classrooms are equipped with the latest teaching aids including LCD Projectors. Department has also filed 47 patents and 484 papers published in reputed journals and conferences.

Also, the Department has provided consultancy of Rs. 14,00,000/- to the companies & institutes. Department also received a research grant of Rs. 3,75,000/- from BCUD Pune. Our student's projects have been nominated and awarded at various reputed competitions and Innovation Challenges. Many of our alumni have become entrepreneurs and succeeded in their business.

About Conference: ICDIDSA-2023 will be held in Hybrid mode at the Department of Electronics and Telecommunication Engineering, Sandip Institute of Technology & Research Centre, Nashik, on November 29th and 30th, 2023, under the theme "Future Technologies for Smart Systems and application ". The conference will feature plenary talks by distinguished researchers and technologists as well as contributed papers from academics and industry professionals. The ICDIDSA-2023 is a platform for technologists, researchers and industry leaders across the globe to share their ideas on emerging technologies and newer solutions that can guide and lead towards a better tomorrow.

Keynote Speakers:

Dr. S.N. Omkar Chief Research Scientist IISC Bangalore	Research Areas: Aerial Vehicles, Biomechanics, Satellite Image Processing & Machine learning
Fl Lt AT Kishore CEO Vidhya Sangha Technologies	Research Area: Artificial Intelligent, Machine learning
Mr. Ravi Desai CEO	AI, Python, Machine Learning



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PT Benua Green Energy Indonesia	
Mr. Ajay Kashyap	

Organizing by Department: Electronics & Telecommunication Engineering

Date of Conference: 29nd & 30th November 2023

Mode of Conference: Hybrid (Offline/Online)

Call for Papers:

ICDIDSA-2023 will be held in Sandip Institute of Technology & Research Centre (NAAC 'A' Grade Accredited) Nashik, Maharashtra, INDIA., during November 29-30, 2023. This conference provides opportunities for the different areas delegates to exchange new ideas and application experiences, to establish business or research relations and to find global partners for future collaboration. We hope that the conference results constituted significant contribution to the knowledge in these up to date scientific field. The organizing committee of conference is pleased to invite prospective authors to submit their original manuscripts to ICDIDSA-2023.

The conference will bring together leading researchers, engineers and scientists in the domain of interest from around the world. Topics of interest for submission include, but are not limited to

Track 1 VLSI	Track 2 Deep learning	Track 3 Communication	Track 4 Drone & Control Instrumentation
Analog, Digital & Mixed Circuit	Machine learning	Mobile Communication	Drone system
MEMS & NEMS	IOT & Cloud Computing	Neural network	Humanoid Robots
VLSI Device & SoC	Data Science	Wireless communication(5g/6g)	Mobile Robotics
MEMS Integrated Sensor	Artificial learning	Network & security	Space & underwater Robot
Bio Electronic Device Circuit	Computer Architecture & system	Optical Communication	Industrial Automation
Semiconductor Device Modelling	Data mining	GPS & GNSS	Control System Modelling
RF Circuit	Computer Graphics	Image & video Processing	Network control System
Flexible &			



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wearable electronic device	Software & database	Signal Processing & Control	Fuzzy control
Smart Material	Multimedia engineering	Pattern recognition	Intelligent control & application

- Authors are invited to submit their original and unpublished work. Topics of interest for submission include under thrust areas of Electronics, Artificial Intelligence and Robotics control Instrumentation (but not limited)
- Paper selected for conference proceeding is directly published in IJETT Journal.
- All papers that conform to submission guidelines will be peer-reviewed and evaluated based on originality, technical and/or research content/depth, correctness, relevance to conference, contributions, and readability. Acceptance of papers will be communicated to authors by email. The authors of the accepted papers will be allowed to make corrections in accordance with the suggestions of the reviewers and submit final camera-ready papers within the stipulated deadline.
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Best Paper Awards

- As evaluated by TPC Chairs and TPC Members, THREE papers will be given Best Paper Award (s) for. The above committee will expect the presentation of the selected best papers during the conference before finalizing the list. The winners will be declared during the conference.

Paper Submission:

Manuscript Style Information:

- Authors should submit Full Length Papers, not just an Abstract Only full-length papers in PDF format will be considered for review not just an Abstract. Full length paper as per format in PDF format will be accepted.
Paper Length: Up to 10 pages, including figures, tables & references. Paper Formatting and Margins: Please carefully follow the template for specifications. No page numbers please. The same will be inserted during proceedings.
Restrict the Similarity Index below 15%
- Please ensure that all the figures are of 300 DPI resolutions.

Please refer to the author templates given below:

Please Adhere to the Author guidelines.



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Proceeding Template – Template attaching with the email

Organizer Committee:

Chief Patron:

- **Dr. SANDIP N. JHA** (Hon'ble Chairman, Sandip University, Nashik)

Patrons

- **PROF. PRAMOD KAROLE** (Academic Facilitator, Sandip Foundation)

Convenor

- **Dr. M.M. Patil** (Principal, SITRC, Sandip Foundation, Nashik)

Conference Chair

- **Dr. Gayatri M. Phade** (Professor & Head, Department of Electronics & Telecommunication Engineering, SITRC, Sandip Foundation, Nashik)

Organizer Coordinator

- Ankur Saxena (Assistant Professor, Electronics & Telecommunication Engineering)

Co-Organizer Coordinator

- Dr.Omkar S. Vaidya (Associate Professor, Electronics & Telecommunication Engineering)
- Dr.Bhagwat Kakde (Associate Professor, Electronics & Telecommunication Engineering)
- Dr.Minal Gade(Associate Professor, Electronics & Telecommunication Engineering)
- Dr. Rahul Awathankar (Assistant Professor, Electronics & Telecommunication Engineering)
- Sushant Pawar (Assistant Professor, Electronics & Telecommunication Engineering)
- Mayur Ingale (Assistant Professor, Electronics & Telecommunication Engineering)
- Ajay Mishra (Assistant Professor, Electronics & Telecommunication Engineering)

Important Dates

Deadline of Full Paper Submission	11 th September 2023
Notification of Acceptance	10 th October 2023
Final Paper Submission	25 th October 2023
Registration Deadline	25 th October 2023
Conference Date	29 th & 30 th November 2023



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How to Apply:

Link for Registration and Paper submission	
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Registration Fees for Conference Proceeding:

Category	Indian Author		Foreign Author	
	IEEE Member	Non- IEEE Member	IEEE Member	Non- IEEE Member
Students (PG/Research scholar)	2000	2500	\$100	\$110
Academician/Industry	3000	3500	\$120	\$130
Only Attending	1000		\$70	

Registration Fees **for Journal Publication:

Category	Indian Author		Foreign Author	
	IEEE Member	Non- IEEE Member	IEEE Member	Non- IEEE Member
Students (PG/Research scholar)	5000	5500	\$100	\$130
Academician/Industry	6000	6500	\$150	\$200

****Only applicable when Paper accepted by Journal**

Account Details:

1	Beneficiary Name	IEEE Student Branch STB20132 SITRC
2	Bank Name	Bank of India
3	Branch Name	Sandip Foundation
4	IFSC Code	BKID0000819
5	Account Number	081910210000023
6	Type of Account	Current
7	MICR Code	422013012

Publication partner:

- IOP Material Science & Engineering**
- AIP Conference Proceeding**



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- IEEE Xplore Digital Library**
- International Journal Emerging Trends in Technology (Indexing -Google Scholar) *

* Accepted

** Waiting for Response

Advisory Committee:

- Dr. Anna Karkanista (Head of the Department of Software programming at Yanka Kupala state University of Grodno Belarus).
- Dr. Svetlin Antonov, Bulgaria (Technical university of Sofia, Faculty of telecommunications, Aofia, Bulgaria).
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- Dr. S.T. Gandhe (Principal of Pune Institute of Computer Technology, Pune, Maharashtra, India)

Contact Details:

Registration Coordinator:

Sushant Pawar	sushant.pawar@sitrc.org	97660 77909
Ajay Mishra	ajay.mishra@sitrc.org	6395 717 759
Minal V. Gade	minal.gade@sitrc.org	98506 75009



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Publication Coordinator:

Dr. Omkar S. Vaidya	omkar.vaidya@sitrc.org	9403163046
Dr. Rahul Awathankur	rahul.awathankar@sitrc.org	97025 09789
Ankur Saxena	ankur.saxena@sitrc.org	9413725430
Dr. Bhagwat Kakde	bhagwat.kakde@sitrc.org	87700 61443

Accommodation Coordinator:

Mayur Ingale	mayur.ingale@sitrc.org	90215 50865
Dr. Bhagwat Kakde	bhagwat.kakde@sitrc.org	87700 61443
Dr. Rahul Awathankur	rahul.awathankar@sitrc.org	97025 09789

For Other queries:

Email id: conference@sitrc.org

Ankur Saxena

+91- 9413725430