



**Sandip Foundation's
Sandip Institute of Technology and Research
Center, Nashik.**

E-Bulletin
January 2019

Staff Co-ordinator:

Prof. Pravin R. Pachorkar

Department of Electrical Engineering

Academic Year 2018-19

Report on Electrical Safety

Event Title: Seminar on Electrical Safety

Event Date: 11th January 2019

Event Conduction Duration: 2 Hrs, 3.00 pm to 5.00 pm

Event Venue: SE- Classroom, Electrical Engg Dept, E Building, SITRC

Event Resource Person Details: 1. Mr.G.Chakurkar, Executive Engineer
2. Mr.Bhausahab Patil, Add.Executive Engineer
3. Mr.Bhushan Patil. Operator
Institute of Training and Safety, Eklahare,Nashik

Name of Event Co-ordinator: Prof.A.V.Satpute. Prof.G.Akotkar

No of Participants: SE,TE and BE- 60 Students

Event Objective & Outcome:

- ✧ To understand safety aspect while working or repairing any electrical equipment's.
- ✧ Importance of ELCB, RCCB , Earthing .
- ✧ Do's and Dont's while performing practical in Lab.
- ✧ Discharging of Electrical equipment before repairing.



Mr.G.Chakurkar explaining the importance of Electrical Safety to Students of SE, TE and BE



Department of Electronics and Telecommunication

NAME OF EVENT: 8th National Level Paper presentation & Technical Fest “**Techxellence 2018**”

DATE OF EVENT: 11th January 2019

EVENT CO-ORDINATOR: Prof. Ms. Swati S. Pawar/Prof. Y. M. Gaikwad

ABOUT EVENT:

Department of Electronics & Telecommunication Engineering of Sandip Institute of Technology & Research Centre, Nashik has organized 8th National Level Paper Presentation and Technical Event for students “**Techxellence 2019**” on Friday, 11th January 2019.

From last seven years “**Techxellence**” has gone from strength to strength, climbing the ladder of success. The objective behind organizing this event will give an overall view of the recent trends and development in various fields of Engineering and Technology to provide an excellent opportunity for the students to exchange the ideas among the students. As per the Culture of Sandip Institute of Technology & Research Centre, Nashik, every academic year Department of Electronics & Telecommunication Engineering organizes National Level Paper presentation & Technical Fest “**Techxellence**” for aspirants assembling from various Polytechnic/Engineering colleges with a very highly competitive spirit to participate and with the strong determination to include their achievements & accomplishments to their resumes.

The winners were encouraged with a cash prizes for all the events. Department feels proud to organize such a successful event and would like to convey thanks to principal and management for their timely advice and cooperation for organizing successful Fest.

OBJECTIVE OF THE EVENT:

- Techxellence has turned the spotlight on emerging topics such as Wireless communication, VLSI Design, RTOS & Embedded System, Digital Signal Processing and Emerging Technologies in the field of Electronics & Telecommunication.
- Paper Presentation allows individuals to demonstrate their knowledge in depth of an area of engineering

- By communicating their research and subsequent opinions to a panel of judges student share the ideas in an interesting and engaging manner.

- The purpose of the Technical Event is to recognize outstanding technical contributions from individual students.

OUTCOME OF THE EVENT:

- Techxellence has provide the opportunities to the new researcher of engineering to present their presentation skills.

- The main advantage or motto of the presenting paper is to bring students out of stage fear and also improve their communication skills.

- The number of papers student present has helped to explore hidden talents in student and identify a positive difference in themselves.

- It also provides an opportunity to learn in detailed about a core subject which is a part of curriculum and develop effective presentation skills.

- Award with a certificate boost the confidence in student and this certificate count enormously at the time of interview.

GUEST FOR INAUGURATION FUNCTION:

Sr. No	Name of the Guest	Organization
✧	Prof. (Dr.) K. T. V. Reddy,	Charmain, IETE (New Delhi)

INTERNAL GUEST FOR INAUGURATION FUNCTION:

Sr. No	Name of the Guest	Organization
1.	Dr. S. T. Gandhe	Principal, SITRC, Nashik.
2.	Dr. P. G. Burade	Dean Academics and Head Electrical Dept.,

		SITRC, Nashik.
3.	Dr. M. M. Patil	Dean Admin, SITRC, Nashik.
4.	Dr. S. N. Patil	HOD, ESH, Department, SITRC, Nashik
5.	Dr. Amol Potgantwar	HOD, Computer, Department, SITRC, Nashik
6.	Dr. Pawan Bhaladhare	HOD, IT, Department, SITRC, Nashik
7.	Dr. P. R. Baviskar	HOD, Mechanical, Department, SITRC, Nashik
8.	Prof. J. P. Nayak	HOD, Civil , Department, SITRC, Nashik
9.	Dr. R. S. Patil	HOD, MBA, Department, SITRC, Nashik

JUDGES OF TECHXELLENC:

Sr. No	Name of the Guest	Organization
1.	Prof. S. J. Pawar	Asst. Prof, E&TC, SIEM, Nashik
2.	Prof. Sarika Patil	Asst. Prof, MBA, SITRC, Nashik

WINNERS OF TECHXELLENC:

Sr. No.	Name of the Student	Name of Institute	Name of the Event	Prize Details First / Second	Prize Amount in Rs.
1	Mr. Vishal Deore & Mr. Hemant Ahire	Matoshri COE, Nashik	Poster	First	1000/-
2	Ms. Bhakti Desai &	SITRC,	Poster	Second	500/-

	Ms. Samruddhi Desai	Nashik			
3	Mr. Mrunal Desale & Mr. Shubham Upadhyay	SIEM, Nashik	Quiz Competition	First	1000/-
4	Mr. Aijaz Malik & Mr. Lalit Attarde	SITRC, Nashik	Quiz Competition	Second	500/-
5	Mr.Sanket Arun Mahajan Mr. Shubham Sandip Chandak Mr. Prathamesh Sanjay Deshmukh Mr. Akash Sanjay Jadhav	SITRC SITRC SITRC SVIT, Chincholi	PubG Tournament	First	1000/-
6	Mr. Shubham Bhaskar Jadhav	KTHM, Nashik	PubG Tournament	Second	500/-
7	Mr. Suchit Patil Mr. Ishwar Patil Mr. Mangesh Chavan Mr. Kenal Sagar	SITRC SITRC SITRC SITRC	Hunted Cricket	First	1000/-
8	Mr.Yash Patil Mr.Adesh Kadam Mr. Prasad Jadhav Mr.Shubham Gajare	SITRC SITRC SITRC SITRC	Hunted Cricket	Second	500/-
9	Mr. Jaysingh Pal Mr. Anshu Vishwakarma	SP SITRC	Circuit Debugging	First	1000/-
10	Mr. Vishal Bhosale Mr. Mahesh Lachake	SITRC SITRC	Circuit Debugging	Second	500/-
11	Mr. Bushra Pathan	SITRC	Paper Presentation (Electrical & E&TC)	First	1000/-
12	Ms. Pooja Vispute		Paper Presentation (Electrical & E&TC)	Second	500/-
13	Ms. Khushi Nikumbh Mr. Neil Narse	GGSP GGSP	Paper Presentation (Computer & IT)	First	1000/-
14	Ms.Athira R.S. Nair Ms. Kirti J. Parde	SP SP	Paper Presentation (Computer & IT)	Second	500/-
15	Mr.Chavan Kanhaiya G.	SIEM	Paper Presentation (Mechanical)	First	1000/-

16	Mr. Abhishek Ksirsagar Mr. Sunny Bhagwat	SIEM SIEM	Paper Presentation (Mechanical)	Second	500/-
17	Mr. Yash Jagtap Mr. Diana J. Fernando	SITRC SITRC	Paper Presentation (Civil)	First	1000/-
18	Mr. Ritik Agrawal Ms. Ashwini Darade	SIEM SIEM	Paper Presentation (Civil)	Second	500/-

PHOTOGRAPHS OF THE EVENT







DEPARTMENT OF INFORMATION TECHNOLOGY

Report of Expert Lecture on “Microprocessor and Microcontroller”

Name of Program: Expert Lecture on Microprocessor and Microcontroller

Venue: SE Classroom, Dept. of I.T., SITRC.

Event Coordinator: Prof. Sagar S. Rajebhosale

Date of Program: 24th January 2019

Audience: SE IT students

Resource Person: Mr. Amit Palve, Assistant Professor-Dept. of Computer Engg, SITRC, Nashik.

Objective

- To impart adequate knowledge of Microprocessor and Microcontroller.
- To develop programming skills using the fundamentals and assembly language programming.
- To discuss the recent issues and applications of Microprocessor and Controller.

Outcomes :

- Students understood the fundamental of Microprocessor and Microcontroller.
- Students understood about the different recent issues of Microprocessor.
- Students will be able to use concepts in Internet of Things.
- Students will be able to apply the appropriate knowledge to create a real world problem's solution.
- Students understood the uses of processors and controllers in real world.

Sandip Institute of Technology & Research Centre "Department of Information Technology Organized Expert Lecture on Microprocessor and Microcontroller for SE-IT students on 24th January 2019. The session was conducted by Mr. Amit Palve at Information Technology Dept, SITRC. Around 50 students attended the Program.

Some Glimpses of the session is as follows:



Mr.Amit Palve delivering the session



Students during the Session

Report of Expert Training Session on “Quantitative Aptitude”

Name of Program: Expert Training Session on Quantitative Aptitude

Venue: BE Classroom, Dept. of I.T., SITRC.

Event Coordinator: Prof. Sagar S. Rajebhosale

Date of Program: 10th January 2019

Audience: TE and BE IT students

Resource Person: Mr. Avinash Aher, Trainer-Professional Academy, Nashik.

Objective

- To impart adequate knowledge on the need of Quantitative Aptitude.
- To develop quantitative aptitude skills using the fundamentals and basics of Mathematics.

Outcomes :

- Students understood the fundamental of aptitude.
- Students understood use of basic mathematical formulas to solve aptitude questions.

- Students will be able to use different tricks to solve aptitude questions.
- Students will be able to apply the basic concepts of mathematics to quantitative aptitude test in placement activities.

Sandip Institute of Technology & Research Centre "Department of Information Technology Organized Expert Training Session on Quantitative Aptitude for TE and BE IT students on 10th January 2019. The session was conducted by Mr. Avinash Aher at IT Dept, SITRC. Around 70 students attended the Program. The session started with the welcoming the trainer- Mr. Avinash Aher by Prof. Mohan Nikam.

Some Glimpses of the session is as follows:



