Faculty Name: Dr. Omkar Suresh Vaidya

Designation: Associate Professor

Mail ID: omkar.vaidya@sitrc.org

BIO

Dr. Omkar S. Vaidya is an Associate Professor in the Electronics and Telecommunication Engineering department of SITRC, Nashik. He completed B.E. (Electronics Engineering) in 2009, M.E. (Communication Engineering) in 2013 and Ph.D. (E&TC Engineering) in March 2020. He has 13 years of teaching experience. His research area is Image Processing, Computer Vision and Machine Learning. He is approved Ph.D. Guide in E&TC Engg. of Savitribai Phule Pune University, Pune. He has filed four patents. He has developed the E-content on laboratory exercises in Electronic Devices and Circuits for staffs and students. He has authored one book with International Publisher and published 33 research papers in National/International Journals with good impact factor. Also, he has presented 32 research papers in National/International Conferences. He has received "Best Paper Award" twice for his research papers in Conferences. He has been awarded as "Best Outstanding Young Researcher Award". He has been honored by various Government and Non-Government Apex bodies. He served as Evaluators for several hackathons and design contests and worked as Reviewers and Session Chairs for many conferences. His biography is published in Asia Pacific Who's Who 2017 directory. He is a Chartered Engineer in Electronics and Telecom division and Senior Member of IEEE. He is associated with professional bodies like IETE, IEI, ISTE, ISCA and IAENG. He has guided 11 PG dissertations and 50+ UG projects. Currently, he is the Dean Research of SITRC, Nashik and collaborated with various research organizations.

Qualification:

1. B.E. (Electronics Engineering)
2. M.E. (Communication Engineering)
3. Ph.D. (Electronics and Telecommunication Engineering)

Paper Publications details:

1. International Journals: 33
2. International Conferences: 17
3. National Conferences: 15
4. Book: 01
5. Article in National Technical Volume: 01

Area Research Work:

He worked as Principal Investigator for research project funded by Savitribai Phule Pune University under Research Mentorship Program. He was mentor of Research Grant for establishment of Startup funded by The Innovation and Entrepreneurship Development Centres (IEDC), Govt. of India. Also, he was supervisor for research project selected for India Innovation Challenge Design Contest organized by AICTE, DST and received the tools free of cost from Texas Instruments to implement the system.

Experience:

1. Teaching experience : 13 Years
2. Research experience : Nil

Subjects:

1. **UG:** Basic Electronic Engineering, Electronic Circuits, Control System, Power Electronics, Digital Image Processing and Wireless Sensor Networks
2. **PG:** Research Methodology, Environmental Studies and CUDA.