

Faculty Name: Y. R. Risodkar

Designation: Assistant Professor

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**BIO**

Yogesh R. Risodkar is an Assistant Professor in the Electronics and Telecommunication Engineering department of SIEM, Nasik, India. He has a responsibility of Head of Department, E&TC since 2019. He got his Masters in Electronics Engineering from Govt. College of Engineering, Aurangabad. His research interest is in Signal Processing, Wireless Communication, Computer Vision, and Machine Learning and in Health care. This includes knowledge graphs and its applications. His research work has been disseminated in several internationally reputed journals and conferences. He has supervised masters' students and served as reviewer for many reputed journals. He has filed, Published 04 patents and also awarded with 01 patent grant by the Govt. of INDIA. He is in process of completion of Project Management Certification by PMI. His peripheral interests lie in activities that include Travel, Music, and Sports.

Qualification:

1. BE (Electronics Engineering)
2. M.E (Electronics Engineering)

Paper Publications details:

1. International Journal: 26
2. International Conference: 17
3. Book Chapter: 00

Experience:

1. Teaching experience : 20 years
2. Research experience : 00

Subjects:

1. Signal & Systems
2. Digital Signal Processing
3. Computer Networks
4. Digital Electronics
5. Electronic Devices and Circuits
6. Linear Integrated Circuits
7. Control Systems, Power Electronics
8. Digital Communication